

In the Abstract:

~~Set of integrated capacitor arrangements, in particular integrated grid capacitors~~

~~An explanation is given of, inter alia, a~~ set of integrated capacitor arrangements is presented, each of which has having a circuitry-effective main capacitor (12) and a connectable correction capacitor (16). The connection enables the capacitance of the capacitor arrangement (10) to be ~~corrected.~~ Each capacitor arrangement has an electrically conductive antifuse connection and antifuse interruption between the correction capacitor and the main capacitor, which are produced after the main capacitor has been formed. The connection and interruption enable the capacitance of the capacitor arrangement to be corrected.